L Number	Hits	Search Text	DB	Time stamp
-	20	365/51,63,99,104,189.03,220,221,230.03,233 and (terminat$3 near resistor) and memory and bus and motherboard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:45
-	7	365/51,63,99,104,189.03,220,221,230.03,233 and (terminat$3 near resistor) and memory and bus and (stub near2 resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:43
-	7	365/51,63,99,104,189.03,220,221,230.03,233 and (terminat$3 near resistor) and bus and (stub near2 resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:42
-	12	365/51,63,99,104,189.03,220,221,230.03,233 and (terminat$3 near resistor) and bus and ((stub near2 resistor) or (series near2 resistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:43
-	14	326/$.ccls. and (terminat$3 near resistor) and memory and bus and motherboard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:47
-	73	(terminat$3 near resistor) and memory and pin and bus and motherboard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:48
-	14	365/$.ccls. and (terminat$3 near resistor) and memory and pin and bus and motherboard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:49
-	7	326/$.ccls. and (terminat$3 near resistor) and memory and pin and bus and motherboard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:50
-	2	326/$.ccls. and (terminat$3 near resistor) and memory and pin and bus and motherboard and (stub with resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:51
-	2	326/$.ccls. and motherboard and (memory with chip) and pin and bus and (terminat$3 near resistor) and (stub with resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:56
-	1	365/$.ccls. and motherboard and (memory with chip) and pin and bus and (terminat$3 near resistor) and (stub with resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:53
-	8	motherboard and (memory with chip) and pin and bus and (terminat$3 near resistor) and (stub with resistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:54
-	5	motherboard and (memory with chip) and pin and bus and (terminat$3 near resistor) and ((stub with resistor) or Rs) and Rterm	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 08:58
-	673	bus and memory and (resist$7 near2 terminat$3) and connector	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:27

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–	146	bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:00
–	15	365/51,63,99,104,189.03,220,221,230.03.ccls and bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:02
–	21	365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:03
–	22	326/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:10
–	33	bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5 and (chip-set or (chip adj set) or chipset)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:11
–	8	365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5 and (chip-set or (chip adj set) or chipset)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:41
–	4	326/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector and pin and mother$5 and (chip-set or (chip adj set) or chipset)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/20 09:11
–	101	365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:28
–	8	365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and pin and connector and mother	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:32
–	93	(365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector) not (365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and pin and connector and mother)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:32
–	1	((365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and connector) not (365/$.ccls. and bus and memory and (resist$7 near2 terminat$3) and pin and connector and mother)) and 326/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:33
–	49	365/63,51,61,53,230.03.ccls. and bus and memory and (resist$7 near2 terminat$3) and board$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 08:44
–	6	365/63,51,61,53,230.03.ccls. and bus and memory and (resist$7 near2 terminat$3) and board$1 and calculat$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 09:33